Amendments to the Specification

Please replace the title of the application with the following amended title.

~~TASK EXECUTION SYSTEM~~ SYSTEM AND METHOD FOR EXECUTING SELECTED TASK BASED ON TASK MANAGEMENT TABLE HAVING AT LEAST ONE TASK AND AT LEAST TWO ASSOCIATED PROCESSORS

On page 1, lines 10 to 16 ([0002] of the published application), please replace the paragraph with the following amended paragraph:

There has ~~hitherto~~ been known a system having a function of changing a task to be processed (refer to, e.g., Patent document 1). In a multiprocessor system, however, if a certain processor falls into stoppage due to a fault, etc., it is impossible to assure an operation of the task processed so far by this processor, resulting in a problem that the operation of the whole system cannot be assured.

On page 6, lines 6 to 23 ([0024] of the published application), please replace the paragraph with the following amended paragraph:

As shown in FIG. 1, ~~the~~ information processing device 100 includes two ~~pieces of~~ processors 110, 120 (of which one processor will hereinafter be referred to as a main execution processor 110, and the other processor will be termed an in-charge-of-stoppage processor 120 ~~for the explanatory convenience's sake~~), a storage device 130 (such as a hard disk device, etc.), [[a]] memory 140, and so forth. Further, ~~the~~ information processing device 100 includes, in some cases, an input device (for example, a key set) for inputting various ~~pieces of~~ information and commands, an image display device (e.g., a liquid crystal display) for displaying a result of processing thereof, a voice output device (for instance, a loudspeaker), etc. ~~(none of these devices are illustrated)~~. Note that although two processors are shown in this exemplary embodiment, ~~the two processors 110, 120 are exemplified for the explanatory convenience's sake, however,~~ the present invention is not limited ~~the~~ to two processors. For example, the present invention can be similarly applied even when three or more ~~pieces of~~ processors are provided.

On page 14, lines 26 to 27 and on page 15, lines 1-9 ([0053] of the published application), please replace the paragraph with the following amended paragraph:

Thus, the stoppage condition of the processor is checked at the operation timing of each task (S205), and hence it follows that surrogation of executing the task is promptly conducted. Further, the task that is to be invariably executed by the main execution processor 110 is assigned beforehand to the in-charge-of-stoppage processor 120, whereby the task can be executed by the pre-assigned processor 120 when the processor <u>110</u> ~~120~~ falls into the stoppage. Accordingly, it is possible to improve the possibility of assuring the operation of the system even when the system is partially stopped.